

Amendments to the Specification:

Page 18, lines 6-11, please replace with the following paragraph:

BI
AK
In the case where the amount of ATM information remaining in the memory 62 is not different from that at the time of the previous frame transmission, however, there is no ~~need~~ need to change the transmission rate for the next frame. Hence, the time slot processor outputs only the frame generating instruction to the frame generator 68 without outputting thereto the TS change information adding instruction.